K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

VII SEMESTER TIME TABLE FOR THE YEAR 2023 (ODD SEMESTER)

W.E.F. : 11/9/2023

SEC : 'B'

CLASS TEACHER : Mrs. Vishalini Divakar

CLASS ROOM : OB LH 311

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	← CN LAB (18ECL76) - B1 / VLSI LAB (18ECL77) - B2 →				CRYPTO (18EC744)	LUNCH BREAK	E&E (18ME751)	VLSI (18EC72)	T
TUE	VLSI (18EC72)	CN (18EC71)	TEA BREAK	CRYPTO (18EC744)	SC (18EC732)		← CN LAB (18ECL76) - B2 / VLSI LAB (18ECL77) - B1 →		
WED	CRYPTO (18EC744)	E&E (18ME751)		SC (18EC732)	VLSI (18EC72)		CRYPTO (18EC744)	CN (18EC71)	T
THU	CN (18EC71)	E&E (18ME751)		SC (18EC732)	VLSI (18EC72)		← VLSI ~~Project Work Phase - 1 (18ECP78)~~ →		
FRI	CRYPTO (18EC744)	SC (18EC732)		CN (18EC71)	E&E (18ME751)		← Project Work Phase - 1 (18ECP78) →		

Sub-Code	Subject Name	Faculty Name
18EC71	Computer Networks	Dr. Dinesh Kumar D S
18EC72	VLSI Design	Mr. Praveen A
18EC732	Satellite Communication **[Professional Elective - 2]**	Mr. Saleem S Tevaramani
18EC744	Cryptography **[Professional Elective - 3]**	Dr. P N Sudha
18ME751	Energy and Environment **[Open Elective -B]**	Mrs. Vishalini Divakar
18ECL76	Computer Networks Lab	Dr. Dinesh Kumar D S , Mrs. Kavya
18ECL77	VLSI Laboratory	Mr. Praveen A-B1, B2, Mrs. Bhanumathi A- B1, Mrs. Anita P -B2
18ECP78	Project Work Phase - 1	Dr. Sudarshan , Mrs. Vishalini Divakar
	Internship	Mr. Santhosh Kumar B R, Mr. Praveen A

Time Table Co-ordinator

HOD

HEAD OF THE DEPARTMENT
Dept. of Electronics & Communication Engg
K.S. Institute of Technology
Bengaluru - 560 109

Principal

PRINCIPAL
K.S. INSTITUTE OF TECHNOLOGY
BENGALURU - 560 109.

K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

VII SEMESTER TIME TABLE FOR THE YEAR 2023 ODD SEMESTER)

W.E.F. : 11/9/2023

SEC : 'A'

CLASS TEACHER : Dr. Dinesh Kumar D S

CLASS ROOM : OB LH 310

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	CRYPTO (18EC744)	SC (18EC732)	TEA	E&E (18ME751)	CN (18EC71)	LUNCH BREAK	← Project Work Phase - 1 (18ECP78) →		
TUE	CRYPTO (18EC744)	E&E (18ME751)		VLSI (18EC72)	CN (18EC71)		CRYPTO (18EC744)	SC (18EC732)	T
WED	← CN LAB (18ECL76) -A1 / VLSI LAB (18ECL77) -A2 →				E&E (18ME751)		SC (18EC732)	VLSI (18EC72)	T
THU	CRYPTO (18EC744)	VLSI (18EC72)	BREAK	CN (18EC71)	E&E (18ME751)		← Project Work Phase - 1 (18ECP78) →		
FRI	CN (18EC71)	VLSI (18EC72)		CRYPTO (18EC744)	SC (18EC732)		← CN LAB (18ECL76) - A2 / VLSI LAB (18ECL77) - A1 →		

Sub-Code	Subject Name	Faculty Name
18EC71	Computer Networks	Dr. Dinesh Kumar D S
18EC72	VLSI Design	Mr. Praveen A
18EC732	Satellite Communication **[Professional Elective - 2]**	Mr. Saleem S Tevaramani
18EC744	Cryptography **[Professional Elective - 3]**	Dr. P N Sudha
18ME751	Energy and Environment **[Open Elective -B]**	Mrs. Vishalini Divakar
18ECL76	Computer Networks Lab	Dr. Dinesh Kumar D S , Mrs. Kavya
18ECL77	VLSI Laboratory	Mr. Praveen A- A2, Mrs. Bhanumathi A-A1,A2 Mrs. Anita P -A2
18ECP78	Project Work Phase - 1	Dr. Sudarshan , Mrs. Vishalini Divakar
	Internship	Mr. Santhosh Kumar B R, Mr. Praveen A

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