



K.S. INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Ph.D.Awarded Faculty

Sl. No	Name of the Faculty	Title of Thesis	University	Year of completion
1	Anita P	Novel Frame Work for Packet Classification with Superior Network and Computational Performance	VTU, Belagavi	February 2023
2	Electa Alice Jayarani A	Audio Watermarking Encompassing Analog and Digital Signal Interconversion	REVA University	January 2023
3	Dinesh Kumar D S	A Novel Multi Algorithm Based Speech and Face Recognition System for The Analysis of FAR & FRR	VTU, Belagavi	April 2021
4	Devika B	Optimizing Power in Ad hoc Networks	VTU, Belagavi	Oct 2020
5	Rekha N	Noise cancellation in GSM Logical Channel using Search Algorithm	VTU, Belagavi	July 2020
6	Chanda V Reddy	Bit Loading Algorithm with Optimizes Bit Rate and Power Allocation in Wireless Communication System	VTU, Belagavi	Mar 2019
7	B Sudarshan	Analysis of Content Organization and Retrieval in Graded Memory	JAIN University, Bangalore	Jan 2017
8	Sangappa S B	A Cross Layer Architecture Based Robust Routing Protocol for Inter-Vehicular Communication VASNET	AISECT, MP	July 2017
9	Surekha Borra	Image Data Hiding for Copyright Protection Based on Visual Cryptography Schemes	JNTU, Hyderabad	Feb 2015
10	P N Sudha	Speech Compression and Error Correction for Mobile Communication	JNTU, Anantapur	Nov 2013

Details of Faculty from 2013

Sl. No	Name of the Faculty	Title of Thesis	University	Year of completion
1	Pooja S	Hybrid Regularization Algorithm for Efficient Blind Image Deblurring	VTU, Belagavi	February 2023
2	Prathima B A	Development of measurement algorithms and communication protocols for automated inspection in closed loop manufacturing system	VTU, Belagavi	Mar 2022
3	Joy Prabhakaran P	Novel Techniques in Image Zooming and Hierarchical Multi Resolution Image Representation	IIIT-B, Bangalore	July 2018
4	Supriya V G	Augmented Security Based on Cancellable Biometric	JAIN University, Bangalore	Dec 2016
5	Benjamin Joseph A	Optimized Feature Preserving Image Compression Technique	ANNA University, Chennai	July 2016
6	R Jayagowri	Power Optimization Techniques for Design for Testability	JNTU, Hyderabad	June 2014
7	Shanthi Prasad M J	Estimation and Suppression of Substrate Noise in Mixed Signal VLSI Chip	VTU, Belagavi	April 2013