**NAME OF THE LAB: HDL LAB -15ECL58**

**LOCATION:** 404 - OB IV Floor

**DESCRIPTION:** This lab has a **total area of 890.56 sq ft.** It is utilized by $5^{th}$ semester students for learning concepts of VHDL and Verilog Programming during the Odd semester of the academic calendar.

**Ambiance:** It is provided with uninterrupted power supply with 30 KVA.

**Safety:** First Aid Kit available. All Electrical wires are protected by Miniature Circuit Breaker and fuses.

**No. of Systems and their specification:** LENOVO SYSTEMS (i-5Processor, RAM- 4GB, Square Monitors), LABVIEW software-8 Users (4 multisim), 8 Port Switch-1 (D-Link), Pattern Generators , FPGA Kits , JTAG Cables , Logic Analyzers and Seven segment Display.

**Software Used: Xilinx ISE Software (Version 10.1)**

**OS Version:** Windows 7, Windows XP and Red Hat Linux

**FACULTY IN-CHARGE:** Mrs. Rekha N

**NON- TEACHING STAFF IN-CHARGE:** Ms. Lakshmi K M.