



K S INSTITUTE OF TECHNOLOGY

Kanakapura Main Road, Raghuvanahalli, Bengaluru-560109

Department of Electronics and Communication Engineering

External Academic Audit Report

Academic Year : 2017-2018

1. Department is having its own calender of events in line with college callender of events.
2. Subject allotment file is maintained.
3. Time-table schedule is made and classes are held as per the time-table.
4. Faculties have maintained their course files as per check list.
5. Student's attendance were taken effectively and the same has been maintained throughout the academic year.
6. Evaluation of blue books and assigning marks for respective courses were furnished as per the scheme and solution defined.
7. Faculty feedback form and result analysis forms of each faculty have been preserved well.
8. CCM's were held according to the prescribed schedule and conducted effectively.
9. Internal audit of each semester has been carried out and proof of the same has been maintained.
10. Budget file is in place and updated.

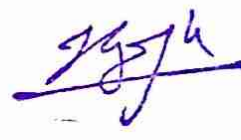

Auditors Signature

Dr. P. Karthik




HOD-ECE

HEAD OF THE DEPARTMENT
Dept. of Electronics & Communication Engg
K.S. Institute of Technology
Bengaluru - 560 109


Principal

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K S INSTITUTE OF TECHNOLOGY

Kanakapura Main Road, Raghuvanahalli, Bengaluru-560109

Department of Electronics and Communication Engineering

External Academic Audit Report

Academic Year : 2017-2018 (Even)

Sl. No.	Particulars to be Verified	Yes/ No
1	Department Calendar of Events File	Yes
2	Subject Allotment Details	Yes
3	Time Table of Teaching and Non-Teaching Staff	Yes
4	Lab/Workshop Utilization Chart	Yes
5	Sample Course File Verification	Yes
6	Maintenance of Attendance Record	Yes
7	Sample Blue Book Verification	Yes
8	Faculty Feedback File	Yes
9	Class Committee Meeting Details (Dates, Agenda, Minutes of Meeting)	Yes
10	Result Analysis File	Yes
11	Internal Audit File	Yes
12	Budget File	Yes

Dr. P. Karthik
Auditor
Dr. P. Karthik



Dr. S. S. S.
19/7
HOD-ECE

HEAD OF THE DEPARTMENT
Dept. of Electronics & Communication Engg
K.S. Institute of Technology
Bengaluru - 560 109

Dr. S. S. S.
19.7.2018
Principal
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BENGALURU - 560 109.



K. S. Institute of Technology - BENGALURU
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 19/3/2018

Name of the Faculty Handling: Dr. P.N Sudha

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC43 / Control Systems	IV / II / A Sec	'Teaching pedagogy' of having extra notes	-	Requested for more number of pouches in course file
2	15EC651 / Cellular Mobile Communication	VI / III / A & B	Each student writing separate topics has been mentioned in 'content beyond syllabus'	Faculty is unable to have 'VTU QP' bcs this course is introduced in VTU for <u>first time</u> .	

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

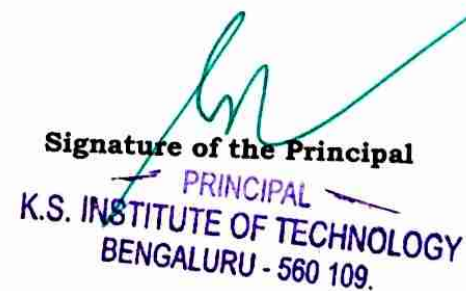
Date of Audit: 19/3/2018

Name of the Faculty Handling: Dr. B Surekha

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC45 / Principles of Communication Systems	IV / II / A	Collaborative learning method in 'Teaching pedagogy'	'Course plan' using LCD details should be submitted	


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

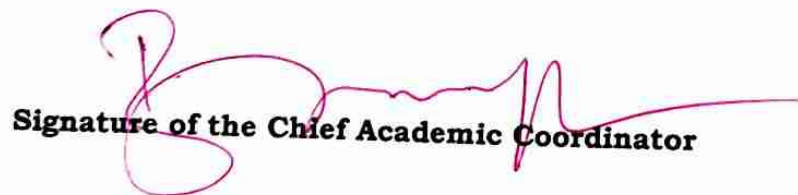
Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 19/3/2018

Name of the Faculty Handling: Dr. Benjamin Joseph A

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	17ELN25 / Basic Electronics	II / I / D	Challenging questions and question bank is exclusively mentioned Assignment questions along with key also being submitted.	In 'Attendance register' signatures are needed	


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

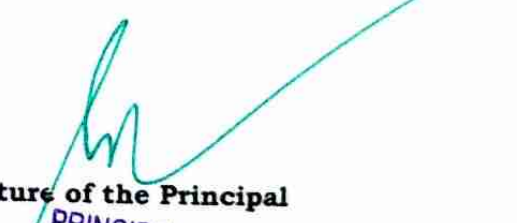
Date of Audit: 19/3/2018

Name of the Faculty Handling: Mrs.Sangeetha.V

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC61 / Digital Communication	VI / III / A	'Course plan' using PS details submitted	'Teaching pedagogy' details has to be submitted	
2	10EC843 / GSM	VIII / IV / B	'Content beyond syllabus' document submitted		
3	Linear ICs and Communication Lab	IV / II / A & C	8 out of 12 exp completed	Format of entering marks for both the cycle of exp cannot be maintained same	


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ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 19/3/2018

Name of the Faculty Handling: Mrs. Prathima. B. A

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC42 / Microprocessor	IV / II / B	'Lesson plan' with signatures available	Level K6 to be rechecked 'Teaching pedagogy' details has to be submitted	
2	15EC45 / Principles of Communication Systems	IV / II / C	'Challenging questions' available	'Teaching pedagogy' details has to be submitted	
3	Microprocessor Lab	IV / II / B & C	Entered marks in Record is in correct format	Marks of each student entered in separate sheet	Viva Questions can be added in manual


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 19/3/2018

Name of the Faculty Handling: Mrs. Aruna Rao. B.P

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC63 / VLSI Design	VI / III / A	VTU and other university QP are available	'Course plan' using LCD details should be submitted 'Teaching pedagogy' details has to be submitted	
2	15EC42 / Microprocessor	IV / II / C	Class notes in correct format	Level K6 has to be rechecked 'Teaching pedagogy' details has to be submitted	
3	Microprocessor Lab	IV / II / B & C	Entered marks in Record is in correct format	Marks of each student entered in separate sheet	Viva Questions can be added in manual


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ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 19/3/2018

Name of the Faculty Handling: Mrs. Veda. B

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC662 / Power Electronics	VI / III / A & B	Assignment questions along with key also being submitted	'Content beyond syllabus' to be submitted 'Student details' needed	
2	15EC64 / Computer Communication Networks	VI / III / B	'Course plan' using LCD details available	'Content beyond syllabus' to be submitted	
3	Computer Networks Lab	VI / III / B	Obs. 5, Viva 5 and Record 10 marks entered	Part A exp will be started soon	Photograph is manual can be included


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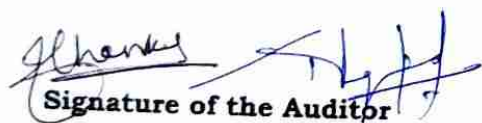
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S


Date of Audit: 20/3/2018

Name of the Faculty Handling: Mrs. Jayasudha B.S.K

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC44 / Signals and Systems	IV / II / A	'Teaching pedagogy' Chart has been prepared	'Course plan' using LCD details should be submitted	
2	15EC62 / ARM Microcontroller & Embedded Systems	VI / III / B	'Content beyond syllabus' document submitted and to be appreciated. 'Teaching pedagogy' Quiz conducted	Faculty is unable to have 'VTU QP' bcs this course is introduced in VTU for first time.	
3	Embedded Controller Lab	VI / III / B	Obs. 5, Viva 5 and Record 10 marks entered	Lab manual need to have viva questions	


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ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S


Date of Audit: 20/3/2018

Name of the Faculty Handling: Mr. Sampath Kumar. S

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	17ELN25 / Basic Electronics	II / I /	Class notes in correct format	'Course plan' using LCD details should be submitted Teaching pedagogy details has to be submitted	Level K4 has to be changed
2	17ELN25 / Basic Electronics	II / I /	Class notes in correct format	'Course plan' using LCD details should be submitted Teaching pedagogy details has to be submitted	Level K4 has to be changed
3	Microprocessor Lab	IV / II / A & B	Entered marks in Record is in correct format	Marks of each student entered in separate sheet	Individual exp marks will be entered


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S


Date of Audit: 20/3/2018

Name of the Faculty Handling: Mr. Sunil Kumar.G.R

SL.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC62 / ARM Microcontroller & Embedded Systems	VI / III / A	Assignment questions along with key also being submitted	'Course plan' using LCD details should be submitted Faculty is unable to have 'VTU QP' bcs this course is introduced in VTU for first time.	Assignment marks will be filled
2	15EC44 / Signals and Systems	IV / II / C	Assignment questions along with key also being submitted. Class notes available	'Course plan' using PS details should be submitted. Teaching pedagogy details has to be submitted	Assignment marks will be filled
3	Embedded Controller Lab	VI / III / A	Obs. 5, Viva 5 and Record 10 marks entered	Lab manual need to have viva questions	


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING


ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 20/3/2018

Name of the Faculty Handling: Mr. Christo Jain S

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC44 / Signals and Systems	IV / II / B	Conducted Surprise test as part of 'Teaching pedagogy'	'Course plan' using LCD & PS details should be submitted	
2	15EC42 / Microprocessor	IV / II / A	'Challenging questions' available	Level K6 has to be rechecked	
3	Microprocessor Lab	IV / II / A & C	Entered marks in Record is in correct format	Marks of each student entered in separate sheet	Viva Questions can be added in manual


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 20/3/2018

Name of the Faculty Handling: Mrs. Nithya Kumari

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC43 / Control Systems	IV / II / B Sec	VTU and other university QP are available	Level K2 has to be changed 'Teaching pedagogy' details has to be submitted	
2	15EC61 / Digital Communication	VI / III / B Sec	'Course plan' using LCD details submitted	'Teaching pedagogy' details has to be submitted	
3	Computer Networks Lab	VI / III / B Sec	Obs. 5, Viva 5 and Record 10 marks entered	Part A exp will be started soon	Photograph in manual can be included


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S


Date of Audit: 20/3/2018

Name of the Faculty Handling: Mrs. Lakshmi H.R

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC663 / Digital System Design using Verilog	VI / III / A	'Teaching pedagogy' details available	'Course plan' using LCD details should be submitted	Level K6 needs to changed
2	15/EC654 / Digital Switching Systems	VI / III / B	'Teaching pedagogy' details available	'Course plan' using LCD details should be submitted	
3	Computer Networks Lab	VI / III / A	Obs. 5, Viva 5 and Record 10 marks entered	Part A exp will be started soon	Photograph is manual can be included


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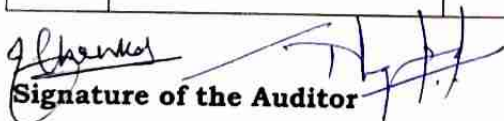
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ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S


Date of Audit: 20/3/2018

Name of the Faculty Handling: Mrs. Priyadarshini J Patil

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	17ELE25 / Basic Electrical Engg	II / I /	'Class notes' available	'Course plan' using PS details should be submitted	'Teaching pedagogy' details has to be submitted
2	15EC43 / Control Systems	IV / II / C Sec	VTU and other university QP are available	'Course plan' using LCD details should be submitted 'Class notes' to be submitted	'Teaching pedagogy' details has to be submitted
3	Seminar	VIII / IV	-	-	-


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
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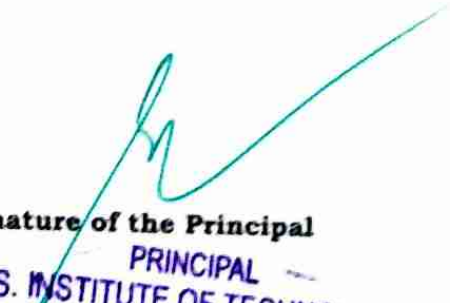
Date of Audit: 20/3/2018

Name of the Faculty Handling: Mr. Deekshith B.N

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	10EC843 / GSM	VIII / IV / A	Assignment questions along with key also being submitted 'Teaching pedagogy' details having but to be submitted	'Course plan' using LCD details should be submitted	
2	Embedded Controller Lab	VI / III / B	Obs. 5, Viva 5 and Record 10 marks entered	Lab manual need to have viva questions	


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**KSIT**


K. S. Institute of Technology - BENGALURU
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S


Date of Audit: 21/3/2018

Name of the Faculty Handling: Dr. B.Sudharshan

SLNo	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	10/EC82 / Digital Switching Systems	VIII / IV / A	Challenging questions are mentioned	'Course plan' using PS details should be submitted Teaching pedagogy details has to be submitted	
2	15EC663 / Digital System Design using Verilog	VI / III / A & B	Challenging questions and question bank is exclusively mentioned	Level K6 has to be changed Teaching pedagogy details has to be submitted	
3	Embedded Controller Lab	VI / III / A	Obs. 5, Viva 5 and Record 10 marks entered	Lab manual need to have viva questions	


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K. S. Institute of Technology - BENGALURU
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S


Date of Audit: 21/3/2018

Name of the Faculty Handling: Mr. B.R.Santhosh Kumar

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	10EC832 / Network Security	VIII /IV /B	Class notes neatly maintained	'Course plan' using LCD details should be submitted Teaching pedagogy details has to be submitted	
2	15EC46 / Linear Integrated Circuits	IV / II / A	CO PO is neatly done	'Course plan' using LCD details should be submitted Teaching pedagogy details has to be submitted	
3	Linear ICs and Communication Lab	IV / II / A	8 out of 12 exp completed	Format of entering marks for both the cycle of exp cannot be maintained same	


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K. S. Institute of Technology - BENGALURU
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 21/3/2018

Name of the Faculty Handling: Mrs. Vishalini Divakar

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	17ELE25 / Basic Electrical Engg	II / I / E	Exhaustive Question Bank mentioned	'Course plan' using PS details should be submitted	Other university QP needed ✓
2	15EC46 / Linear Integrated Circuits	IV / II / B	Assignment questions along with key also being submitted	'Course plan' using LCD details should be submitted	Other university QP needed ✓
3	Linear ICs and Communication Lab	IV / II / B & C	8 out of 12 exp completed	Format of entering marks for both the cycle of exp cannot be maintained same	


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**KSIT**


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 21/3/2018

Name of the Faculty Handling: Mr. Praveen.A

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC64 / Computer Communication Networks	VI / III / A	'Teaching pedagogy' details submitted is excellent	'Course plan' using LCD details should be submitted	
2	15EC63 / VLSI Design	VI / III / B	'Class notes' available	'Course plan' using LCD details should be submitted 'Content beyond syllabus' to be submitted	
3	Computer Networks Lab	VI / III / A	Obs. 5, Viva 5 and Record 10 marks entered	Part A exp will be started soon	Photograph is manual can be included


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ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 21/3/2018

Name of the Faculty Handling: Mr. Saleem S Tevaramani

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	10EC832 / Network Security	VIII / IV / A	'Class notes' available	'Course plan' using LCD details should be submitted	
2	10/EC82 / Digital Switching Systems	VIII / IV / B	'Class notes' available	'Course plan' using LCD details should be submitted	


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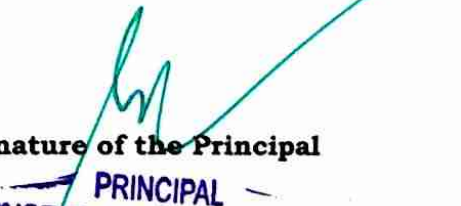
Date of Audit: 21/3/2018

Name of the Faculty Handling: Mrs. Pragati. P

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC64/ Digital Switching Systems	VI / III / A	CO PO is neatly done	'Course plan' using LCD details should be submitted 'Teaching pedagogy' details has to be submitted	
2	15EC46 / Linear Integrated Circuits	IV / II / C	CO PO is neatly done 'Content beyond syllabus' document related to project has been submitted	'Course plan' using LCD details should be submitted 'Teaching pedagogy' details has to be submitted	
3	Linear ICs and Communication Lab	IV / II / B & C	8 out of 12 exp completed	Format of entering marks for both the cycle of exp cannot be maintained same	


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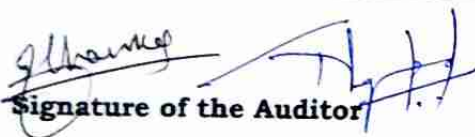
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 21/3/2018

Name of the Faculty Handling: Mrs. Sahana Salagare

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	15EC45 / Principles of Communication Systems	IV / II / B	'Teaching pedagogy' Quiz details submitted	'Course plan' using LCD details should be submitted	
2	17ELN25 / Basic Electronics	II / I / B	'Class notes' available	'Course plan' using LCD details should be submitted	Level K4 will be changed
3	Linear ICs and Communication Lab	IV / II / A & B	8 out of 12 exp completed	Format of entering marks for both the cycle of exp cannot be maintained same	Level K3 will be changed


Signature of the Auditor


Signature of the Chief Academic Coordinator


Signature of the Principal
PRINCIPAL
K.S. INSTITUTE OF TECHNOLOGY
BENGALURU - 560 109.



K. S. Institute of Technology - BENGALURU
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC AUDIT FACULTY REPORT

Auditor Name: M. Umashankar / Nagaprasad K S

Date of Audit: 22/3/2018

Name of the Faculty Handling: Dr. Sangappa.S.B

Sl.No	Course Code/Title & Lab code/ Title	Sem/Year/Section	Strength noticed during auditing	Weakness noticed during auditing	Suggestion for improvements
1	10EC81 / Wireless Communication	VIII / IV / A	Content beyond syllabus has been mentioned	'Course plan' using LCD details should be submitted. Level K3 has to be changed	Two separate files to be kept immediately
2	10EC81 / Wireless Communication	VIII / IV / B	Content beyond syllabus has been mentioned	'Course plan' using LCD details should be submitted	


Signature of the Auditor


Signature of the Chief Academic Coordinator


Signature of the Principal
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K S INSTITUTE OF TECHNOLOGY

Kanakapura Main Road, Raghuvanahalli, Bengaluru-560109

Internal Academic Audit Report

Department: Electronics and Communication Engineering

Academic Year: 2017-2018 (odd)

1. Course files are maintained properly.
2. Personal files are maintained and updated as per the check list.
3. Question banks are not prepared by some of the faculties.
4. Few faculties have prepared their lesson plans.
5. Faculties have to incorporate pedagogies in their teaching-learning process.

Auditors Signature


5/1/2018

5/1/2018



HOD-ECE

HEAD OF THE DEPARTMENT
Dept. of Electronics & Communication Engg
K.S. Institute of Technology
Bengaluru - 560 109



K S INSTITUTE OF TECHNOLOGY

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Internal Academic Audit Report

Department: Electronics and Communication Engineering

Academic Year: 2017-2018 (Odd)

Sl. No.	Name of the Staff	Sem & Section	Subject Name & Code	Strength noticed during Auditing	Weakness noticed during Auditing
1	Dr. P.N. SUDHA	III A	Network Analysis 15EC34	Assignment Questions with scheme, Question Bank	
		V B	Information Theory & Coding 15EC54	Challenging Questions, Question Bank	
		Personal File		Updated	
2	Dr. SHANTHI PRASAD M.J	III A	Engineering Electromagnetics 15EC36	IA Question Papers	
		V A	Fundamentals of CMOS VLSI 10EC56	IA Question Papers	
		VII A & B	VLSI Lab 15ECL77	IA marks entered into registers	
		Personal File		Updated	
3	Dr. SUREKHA B	VII B	Image Processing 10EC763	Class Test, Teaching Pedagogy	
		VII A	DSP Algorithms & Architecture 10EC751	Class Test, Teaching Pedagogy	
		V A	DSP Lab 15ECL57	IA marks entered into registers	
		Personal File		Updated	
4	Mr. K.V. MADHAVA MURTHY	I E & F	Basic Electronics 15ELN15	IA Question Papers	
		VA	Microwaves and Radar 10EC54	Question Bank	
		III A & B	Analog Electronics Lab 15ECL37	IA marks entered into registers	
		Personal File		Updated	
5	Dr. SANGAPPA S.B	V A & B	Management and Entrepreneurship 15EC51	IA Question Papers	
		Personal File		Updated	

6	Mr. B. SUDHARSHAN	V B	Verilog HDL 15EC53	IA Question Papers, Teaching Pedagogy	Lesson Plan
		V A	Operating System 15EC553	IA Question Papers, Teaching Pedagogy	Question Bank
		V B	HDL Lab 15ECL58	IA marks entered into registers	
		Personal File		Updated	
7	Mr. SANTHOSH KUMAR B.R	III B	Analog Electronics 15EC32	IA Question Papers, Teaching Pedagogy	Lesson Plan
		VII A	Embedded System Design 10EC74	IA Question Papers, Teaching Pedagogy	Question Bank
		III B & C	Analog Electronics Lab 15ECL37	IA marks entered into registers	
		Personal File		Updated	
8	Mrs. VISHALINI DIVAKAR	I D	Basic Electrical Engineering (15ELE15)	IA Question Papers	Lesson Plan
		VII A	Power Electronics 10EC73	IA Question Papers	Lesson Plan
		VII A	Power Electronics Lab 10ECL78	IA marks entered into registers	
		Personal File		Updated	
9	Mrs. JAYASUDHA B.S.K	V A	Digital Signal Processing 15EC52	Lesson Plan	Question Bank
		VII B	DSP Algorithms & Architecture 10EC751	Lesson Plan	Question Bank
		V A	DSP Lab 15ECL57	IA marks entered into registers	
		Personal File		Updated	
10	Mr. SAMPATH KUMAR S.	III A	Object Oriented Programming Using C++ 15EC562	IA Question Papers	Question Bank
		V A	Electronic Instrumentation 15EC35	Assignment Questions	Lesson Plan
		V A	HDL Lab 15ECL58	IA marks entered into registers	
		Personal File		Updated	

11	Mr. SUNIL KUMAR G.R.	V A	Verilog HDL 15EC53	IA Question Papers	Lesson Plan
		V A & B	HDL Lab 15ECL58	IA marks entered into registers	
		Personal File		Updated	
12	Mrs. SANGEETHA V.	VB	Digital Signal Processing 15EC52	Lesson Plan	Question Bank
		VII B	Optical Fiber Communication 10EC72	Lesson Plan	Question Bank
		Personal File		Updated	
13	Mr. PRAVEEN A.	V A	Information Theory & Coding 15EC54	IA Question Papers	Question Bank
		VII A	Computer Communication Networks 10EC71	IA Question Papers	Question Bank
		VII A	VLSI Lab 10ECL77	IA marks entered into registers	
		Personal File		Updated	
14	Mr. CHRISTO JAIN	III C	Network Analysis 17EC34	Lesson Plan, Teaching Pedagogy	
		VII B	Power Electronics 10EC73	Lesson Plan, Teaching Pedagogy	
		VII B	Power Electronics Lab 10ECL78	IA marks entered into registers	
		Personal File		Updated	
15	Mr. SALEEM S TEVARAMANI	VII B	Computer Communication Networks 10EC71	IA Question Papers	Question Bank
		VII A	Image Processing 10EC763	IA Question Papers	Question Bank
		III B & C	Digital Electronics Lab 17ECL38	IA marks entered into registers	
		V A & B	Analog Communication Lab + LIC Lab 10ECL58	IA marks entered into registers	
		Personal File		Updated	
16	Mrs. PRATHIMA B.A	III B	Digital Electronics 15EC33	Assignment Questions, IA Question Papers	Lesson Plan
		VII B	Embedded System Design 10EC74	Assignment Questions, IA Question Papers	Lesson Plan
		III A & B	Digital Electronics Lab 17ECL38	IA marks entered into registers	
		Personal File		Updated	

17	Mrs. PRAGATHI P.	III A	Analog Electronics 15EC32	Lesson Plan, Question Bank	Assignment Questions
		III B	Engineering Electromagnetics 15EC36	Lesson Plan, Question Bank	Assignment Questions
		III A & C	Analog Electronics Lab 15ECL37	IA marks entered into registers	
		Personal File		Updated	
18	Mrs. SAHANA SALAGARE	III C	Electronic Instrumentation 15EC35	IA Question Papers	Lesson Plan
		VII A	Optical Fiber Communication 10EC72	IA Question Papers	Lesson Plan
		VII A	Power Electronics Lab 10ECL78	IA marks entered into registers	
		Personal File		Updated	
19	Mrs. LAKSHMI H.R	V B	Operating System 15EC553	Lesson Plan	Question Bank
		VII A & B	Applied Embedded System Design 10EC755	Lesson Plan	
		VII B	VLSI Lab 10ECL77	IA marks entered into registers	
		Personal File		Updated	
20	Mrs. PRIYADHARSHINI. J. PATIL	I B	Basic Electrical Engineering 15ELE15	IA Question Papers	Lesson Plan
		III B	Electronic Instrumentation 15EC35	IA Question Papers	Lesson Plan
		VII B	Power Electronics Lab 10ECL78	IA marks entered into registers	
		Personal File		Updated	
21	Mrs. NITHYA KUMARI R	III A	Network Analysis 15EC34	IA Question Papers	Lesson Plan
		III B	Digital Electronics 15EC33	IA Question Papers	Lesson Plan
		III A & B	Digital Electronics Lab 15ECL38	IA marks entered into registers	
		Personal File		Updated	

22	Mrs. ARUNA RAO B P	V B	Object Oriented Programming Using C++ 15EC562	Lesson Plan	Question Bank
		III C	Analog Electronics 15EC32	Lesson Plan	Question Bank
		III C & A	Analog Electronics LAB 15ECL37	IA marks entered into registers	
		Personal File		Updated	
23	Mr. DEEKSHITH B N	III C	Engineering Electromagnetics 15EC36	IA Question Papers	Lesson Plan
		V B	Digital Signal Processing Lab 15ECL57	IA marks entered into registers	
		III C	Analog Electronics Lab 15ECL37	IA marks entered into registers	
		Personal File		Updated	
24	Mrs. VEDA B	III C	Digital Electronics 15EC33	Lesson Plan	Teaching Pedagogy
		III A & B	Analog Electronics LAB 15EC37	IA marks entered into registers	
		III A & C	Digital Electronics Lab 15ECL38	IA marks entered into registers	
		Personal File		Updated	

1) M. Umathankar

[Signature]
5/1/2018

2) *[Signature]*
5/1/2018
Nadabansad K. S
Auditors Signature



[Signature]
5/1/2018

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