



K S INSTITUTE OF TECHNOLOGY
Kanakapura Main Road, Raghuvanahalli, Bengaluru-560109

Department of Electronics and Communication Engineering

External Academic Audit Report

Academic Year : 2018-2019

1. Faculties have maintained their course files as per check list.
2. Student's attendance were taken effectively and the same has been maintained throughout the academic year.
3. Evaluation of blue books and assigning marks for respective courses were furnished as per the scheme and solution defined.
4. CCM's were held according to the prescribed schedule and conducted effectively.
5. Student mentor process is in place.
6. Faculty feedback form and result analysis forms of each faculty have been preserved well.
7. Internal audit of each semester has been carried out and proof of the same has been maintained.
8. Upgrade Microcontroller laboratoy as per new syllabus.
9. Encourage faculties to enroll for online courses to upgrade their knowledge and skills.
10. Department have to organize state/ national level events.

Auditors Signature

lyn
15/7/19
Dr. hirish Attimavada
Professor & Head
.. of Electronics & Communication Engineering
S. School of Engineering & Management
Bangalore-560 109

AP
15/7/19
Dr. P. Kasthik

Dmy
15/7
HOD-ECE

HEAD OF THE DEPARTMENT
Dept. of Electronics & Communication Engg
K.S. Institute of Technology
Bengaluru - 560 109

15.7.15
Principal



K S INSTITUTE OF TECHNOLOGY
Kanakapura Main Road, Raghuvanahalli, Bengaluru-560109
Department of Electronics and Communication Engineering

External Academic Audit Report

Academic Year : 2018-2019

Sl. No.	Particulars to be Verified	Yes/ No
1	Sample Course File	Yes
2	Studentance Attendance Record	Yes
3	Sample Blue Books	Yes
4	Class Committee Meeting Record	Yes
5	Student Mentor Process	Yes
6	Faculty Feedback	Yes
7	Result Analysis File	Yes
8	Internal Audit Proof	Yes

Auditor

Dr. Gurish V. A. Girimani
15/7/19
Professor & Head
Dept. of Electronics & Communication Engineering
K. S. School of Engineering & Management
Bangalore-560 109
Dr. P. Karthik
15/7/19

HOD-ECE

D. Mys
15/7
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Bengaluru - 560 109

Principal

[Signature]
15.7.19



K S INSTITUTE OF TECHNOLOGY

Kanakapura Main Road, Raghuvanahalli, Bengaluru-560109

Internal Academic Audit Report

Department: Electronics and Communication Engineering

Academic Year: 2018-2019 (Even)

1. Course files are maintained properly.
2. Personal files are maintained and updated as per the check list.
3. Question banks and challenging questions are missing in few files.
4. Faculties have to incorporate more pedagogies in their teaching-learning process.

Auditors Signature

Deblina
10/7/2019

Dept. of Telecommunication Engg.
K. S. Institute of Technology
Bengaluru - 560 109

P. S. Rao
HOD-ECE

HEAD OF THE DEPARTMENT
Dept. of Electronics & Communication Engg
K.S. Institute of Technology
Bengaluru - 560 109



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Kanakapura Main Road, Raghuvanahalli, Bengaluru-560109



Internal Academic Audit Report

Department: Electronics and Communication Engineering

Academic Year: 2018-2019 (Even)

Sl. No.	Name of the Staff	Sem & Section	Subject Name & Code	Strength noticed during Auditing	Weakness noticed during Auditing
1	Dr. P.N. SUDHA	IV A	Control Systems (17EC43)	Tutorial classes Conducted, Challenging Questions	
		VI A&B	Cellular Mobile Communication (15EC651)	Question Bank, Content Beyond Syllabus	
		Personal File		Updated	
2	Dr. SUREKHA B	IV A & C	Principles of Communication Systems 17EC44	Lesson Plan, Assignment Questions	
		Personal File		Updated	
3	Dr. SANGAPPA S.B	VIII A	Wireless Cellular and LTE 4G Broadband (15EC81)	IA Question Paper with Scheme	
		Personal File		Updated	
4	Dr. JOY PRABHAKARAN P.	VIII A, B & C	PROGRAMMING LANGUAGE	Content Beyond Syllabus	
		Personal File		Updated	
5	Dr. B. SUDHARSHAN	VI A	ARM Microcontroller & Embedded Systems 15EC62	Assignment Questions, Challenging Questions	
		IV B	Signals and Systems 17EC42	Lesson Plan, Question Bank	Teaching Pedagogy
		VI A	Embedded Controller Lab 15ECL67	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	

6	Mr. SANTHOSH KUMAR B.R	IV A	Linear Integrated Circuits 17EC45	Assignment Questions, Slow Learners	Teaching Pedagogy
		VI B	Digital System Design using Verilog 15EC663	Question Bank	
		IV A	Linear ICs and Communication Lab 17ECL48	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
7	Mrs. VISHALINI DIVAKAR	II E	Basic Electrical Engineering (18ELE23)	IA Question Paper with Scheme	
		IV B	Linear Integrated Circuits 17EC45	Assignment Questions	
		II E	Basic Electrical Engineering Laboratory (18ELE27)	All the experiments completed. Marks are entered in attendance register.	
		IV B	Linear ICs and Communication Lab 17ECL48	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
8	Mrs. JAYASUDHA B.S.K	VI B	ARM Microcontroller & Embedded Systems 15EC62	Teaching Pedagogy, Question Bank	
		VIII A&B	Machine learning 15EC834	IA Question Paper with Scheme	Challenging Questions
		VI B	Embedded Controller Lab 15ECL67	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
9	Mr. SAMPATH KUMAR S.	II G	Basic Electronics (18ELN24)	Assignment with Scheme	Teaching Pedagogy
		II B	Basic Electrical Engineering 18ELE23	IA Question Paper with Scheme	
		II F & G	Basic Electrical Engineering Laboratory (18ELE27)	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	

10	Mr. SUNIL KUMAR G.R.	IV C	Microprocessor 17EC46	Tutorial classes Conducted, Challenging Questions	
		IV A,B & C	Microprocessor Lab 17ECL47	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
11	Mrs. SANGEETHA V.	VI B	Digital Communication 15EC61	Teaching Pedagogy, Question Bank	
		VIII B	Wireless Cellular and LTE 4G Broadband 15EC81	Lesson Plan, Assignment Questions	
		VI B	Computer Networks Lab 15ECL68	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
12	Mr. PRAVEEN A.	VI A	VLSI Design 15EC63	IA Question Paper with Scheme	
		VI B	Computer Communication Networks 15EC64	Assignment Questions	
		VI A & B	Computer Networks Lab 15ECL68	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
13	Mr. CHRISTO JAIN	IV A	Microprocessor 17EC46	Challenging Questions, Question Bank	
		IV B	Principles of Communication Systems 17EC44	Slow Learners, Question Bank	Teaching Pedagogy
		IV A & C	Microprocessor Lab 17ECL47	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
14	Mr. SALEEM S TEVARAMANI	VI A	Computer Communication Networks 15EC64	Tutorial classes Conducted	
		VIII B	Radar Engineering 15EC833	Challenging Questions, Question Bank	
		Personal File		Updated	
15	Mrs. NITHYA KUMARI R	VI A	Digital Communication 15EC61	Lesson Plan, Question Bank	
		IV C	Control Systems (17EC43)	Assignment Questions with Scheme	
		VI A & B	Computer Networks Lab 15ECL68	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	

16	Mrs. PRAGATHI P.	IV A	Signals and Systems 17EC42	Lesson Plan, Tutorial classes Conducted	
		IV C	Linear Integrated Circuits 17EC45	IA Question Paper with Scheme	
		IV B & C	Linear ICs and Communication Lab 17ECL48	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
17	Mrs. SAHANA SALAGARE	VIII A	Radar Engineering 15EC833	Challenging Questions, Question Bank	Teaching Pedagogy
		II A	Basic Electronics (18ELN24)	Lesson Plan, Tutorial classes Conducted	
		IV A,B&C	Linear ICs and Communication Lab 17ECL48	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
18	Mrs. LAKSHMI H.R	VI B	VLSI Design 15EC63	Teaching Pedagogy, Question Bank	
		VIII A	Fiber Optics & Networks 15EC82	Assignment Questions with Scheme	
		VI B	Embedded Controller Lab 15ECL67	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
19	Mrs. ARUNA RAO B P	IV B	Microprocessor 17EC46	Assignment Questions with Scheme	
		VI A	Digital System Design using Verilog 15EC663	Teaching Pedagogy, Question Bank	
		IV B & C	Microprocessor Lab 17ECL47	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
20	Mrs. VEDA B	IV C	Signals and Systems 17EC42	Tutorial Classes Conducted Question Bank	Teaching Pedagogy
		VI	Digital Switching Systems 15EC654	Assignment Questions with Scheme, Question Bank	
		VI A	Computer Networks Lab 15ECL68	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	

21	Mrs. SHILPA V	II C	Basic Electronics (18ELN24)	Teaching Pedagogy, Question Bank	
		IV B	Control Systems 17EC43	Tutorial classes Conducted, Challenging Questions	
		VI A	Embedded Controller Lab 15ECL67	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
22	Mrs. BHARGAVI ANANTH	VIII B	Fiber Optics & Networks 15EC82	Question Bank, Assignment Question with Scheme	
		IV A & C	Linear ICs and Communication Lab 17ECL48	All the experiments completed. Marks are entered in attendance register.	
		II F & G	Basic Electrical Engineering Laboratory 18ELEL27	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	
23	Mrs. SINDHU S S	II F	Basic Electrical Engineering 18ELE23	IA Question Paper with Scheme	Teaching Pedagogy
		II E,F,G	Basic Electrical Engineering Laboratory 18ELEL27	All the experiments completed. Marks are entered in attendance register.	
		Personal File		Updated	

Rekha N.
10/7/2019.
Auditors Signature

Dept. of Telecommunication Engg
K. S. Institute of Technology
Bangaluru - 560 109

D. Mys
10/7/19

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K S INSTITUTE OF TECHNOLOGY

Kanakapura Main Road, Raghuvanahalli, Bengaluru-560109

Internal Academic Audit Report

Department: Electronics and Communication Engineering

Academic Year: 2018-2019 (odd)

1. Course files are maintained properly.
2. Personal files are maintained and updated as per the check list.
3. Identification of slow learners are missing in few files.
4. Faculties have to incorporate more pedagogies in their teaching-learning process.

Auditors Signature

Debb N
17/1/2019.

[Signature]
17/1/19
Dept. of Telecommunication Engg
K. S. Institute of Technology
Bengaluru - 560 109

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17/1
HOD-ECE

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Internal Academic Audit Report

Department: Electronics and Communication Engineering

Academic Year: 2018-2019 (Odd)

Sl. No.	Name of the Staff	Sem & Section	Subject Name & Code	Strength noticed during Auditing	Weakness noticed during Auditing
1	Dr. P.N. SUDHA	VII A & B	Microwave and Antennas 15EC71	Lesson Plan, Question Bank	
		Personal File		Updated	
2	Dr. SUREKHA B	VII A & B	Digital Image Processing 15EC72	Assignment Questions with Sheme, Challenging Questions	
		Personal File		Updated	
3	Dr. SANGAPPA S.B	V B	Management and Entrepreneurship Development 15ES51	IA Question Paper with Scheme	
		Personal File		Updated	
4	Dr. JOY PRABHAKARAN P.	V A & B	Object Oriented Programming Using C++ 15EC562	Content Beyond Syllabus, Assignments	
		VII A	Advanced Communication Lab 15ECL76	Registers Updated with IA marks	
		Personal File		Updated	
5	Dr. B. SUDHARSHAN	V A	Verilog HDL 15EC53	Content Beyond Syllabus, Assignments	
		V B	Operating System 15EC553	Tutorial classes Conducted, Challenging Questions	Teaching Pedagogy
		V A	HDL Lab 15ECL58	Registers Updated with IA marks	
		Personal File		Updated	
6	Mr. SANTHOSH KUMAR B.R	III A	Analog Electronics 17EC33	Lesson Plan, Question Bank	
		V A & B	8051 Microcontroller 15EC563	Tutorial classes Conducted	Teaching Pedagogy
		III A	Analog Electronics Lab 17ECL37	Registers Updated with IA marks	
		Personal File		Updated	

7	Mrs. VISHALINI DIVAKAR	I D	Basic Electrical Engineering (18ELE13)	IA Question Paper with Scheme	
		I A B & D	Basic Electrical Engineering Laboratory (18ELEL17)	Registers Updated with IA marks	
		Personal File		Updated	
8	Mrs. JAYASUDHA B.S.K	III B	Engineering Electromagnetics 17EC36	Teaching Pedagogy	
		V B	Digital Signal Processing 15EC52	Question Bank	Identification of Slow Learners
		V B	DSP Lab 15ECL57	Registers Updated with IA marks	
		Personal File		Updated	
9	Mr. SAMPATH KUMAR S.	I F	Basic Electronics (18ELN14)	Question Bank	Teaching Pedagogy
		III B	Electronic Instrumentation 17EC32	IA Question Paper with Scheme	Identification of Slow Learners
		III A	Digital Electronics Lab 17ECL38	Registers Updated with IA marks	
		Personal File		Updated	
10	Mr. SUNIL KUMAR G.R.	V A	Information Theory & Coding 15EC54	Lesson Plan, Question Bank	Teaching Pedagogy
		V B	HDL Lab 15ECL58	Registers Updated with IA marks	
		Personal File		Updated	
11	Mrs. SANGEETHA V.	III A	Digital Electronics 17EC34	Lesson Plan, Question Bank	
		VII B	Satellite Communication 15EC755	Assignment Questions with Sheme, Challenging Questions	
		III A	Digital Electronics Lab 17ECL38	Registers Updated with IA marks	
		Personal File		Updated	
12	Mr. PRAVEEN A.	V B	Information Theory & Coding 15EC54	IA Question Paper with Scheme	
		VII A & B	VLSI Lab 15ECL77	Registers Updated with IA marks	
		Personal File		Updated	

13	Mr. CHRISTO JAIN	III B	Network Analysis 17EC35	Assignment Questions with Sheme, Challenging Questions	
		VII A	Power Electronics 15EC73	IA Question Paper with Scheme	
		V A	DSP Lab 15ECL57	Registers Updated with IA marks	
		Personal File		Updated	
14	Mr. SALEEM S TEVARAMANI	VII A	Satellite Communication 15EC755	Content Beyond Syllabus, Assignments	
		VII B	Cryptography 15EC744	Question Bank, Content Beyond Syllabus	
		VII B	Advanced Communication Lab 15ECL76	Registers Updated with IA marks	
		Personal File		Updated	
15	Mrs. NITHYA KUMARI R	III A	Network Analysis 17EC35	Teaching Pedagogy, Tutorial classes Conducted	
		V A	Digital Signal Processing 15EC52	Lesson Plan, Question Bank	
		V A	DSP Lab 15ECL57	Registers Updated with IA marks	
		Personal File		Updated	
16	Mrs. PRAGATHI P.	III B	Analog Electronics 17EC33	Lesson Plan, Challenging Questions	
		III A	Engineering Electromagnetics 17EC36	Assignment Questions with Sheme, Challenging Questions	
		III B	Analog Electronics Lab 17ECL37	Registers Updated with IA marks	
		Personal File		Updated	
17	Mrs. SAHANA SALAGARE	I E	Basic Electronics 18ELN14	Question Bank, Tutorial classes Conducted	
		III B	Analog Electronics Lab 17ECL37	Registers Updated with IA marks	
		VII A	Advanced Communication Lab 15ECL76	Registers Updated with IA marks	
		Personal File		Updated	
18	Mrs. LAKSHMI H.R	V A	Operating System 15EC553	Tutorial classes Conducted	
		VII A	Cryptography 15EC744	Question Bank	
		VII A	VLSI Lab 15ECL77	Registers Updated with IA marks	
		Personal File		Updated	

19	Mrs. PRIYADHARSHINI. J. PATIL	I B	Basic Electrical Engineering 18ELE13	IA Question Paper with Scheme	
		I B,C & D	Basic Electrical Engineering Lab 18ELE17	Registers Updated with IA marks	
		III B	Digital Electronics Lab 17ECL38	Registers Updated with IA marks	
		Personal File		Updated	
20	Mrs. ARUNA RAO B P	V B	Verilog HDL 15EC53	Teaching Pedagogy, Tutorial classes Conducted	
		VII A & B	Digital System Design Using Verilog 15EC663	Question Bank	
		V B	HDL Lab 15ECL58	Registers Updated with IA marks	
		Personal File		Updated	
21	Mrs. VEDA B	III B	Digital Electronics 17EC34	Assignment Questions with SHEME, Question Bank	
		VII B	Power Electronics 15EC73	IA Question Paper with Scheme	
		III B	Digital Electronics Lab 17ECL38	Registers Updated with IA marks	
		Personal File		Updated	
22	Mr. DEEKSHITH B N	III A	Electronic Instrumentation 17EC32	IA Question Paper with Scheme	
		III B	Analog Electronics Lab 17ECL37	Registers Updated with IA marks	
		V B	DSP Lab 15ECL57	Registers Updated with IA marks	
		Personal File		Updated	
23	Mrs. SHILPA V	I C	Basic Electrical Engineering	Assignment Questions, Question Bank	
		I C	Basic Electrical Engineering Lab 18ELE17	Registers Updated with IA marks	
		VII B	VLSI Lab 15ECL77	Registers Updated with IA marks	
		III A	Digital Electronics Lab 17ECL38	Registers Updated with IA marks	
		Personal File		Updated	

24	Mrs. BHARGAVI ANANTH	V A	Management and Entrepreneurship Development 15ES51	Assignment Questions with scheme, Question Bank	
		III A & B	Analog Electronics Lab 17ECL37	Registers Updated with IA marks	
		VII B	Advanced Communication Lab 15ECL76	Registers Updated with IA marks	
		Personal File		Updated	
25	Mrs. SINDHU S S	I A	Basic Electrical Engineering 18ELE13	IA Question Paper with Scheme	
		I A & C	Basic Electrical Engineering Laboratory 18ELEL17	Registers Updated with IA marks	
		III A	Analog Electronics Lab 17ECL37	Registers Updated with IA marks	
		Personal File		Updated	

Deek N.
17/1/2019.

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17/1/2019.

Auditors Signature

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[Signature]
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