

External Academic Audit Report

for

Department of Electronics and Communication Engineering, KSIT, Bengaluru-109

Academic Year: 2019-2020

Sl. No.	Particulars to be Verified	Yes/ No
1	Sample Course File	YES
2	Student Attendance Record	YES
3	Sample Blue Books	YES
4	Class Committee Meeting Record	YES
5	Student Mentor Process	NO
6	Faculty Feedback	YES
7	Result Analysis File	YES
8	Internal Audit Proof	YES

online mentoring
was done for
slow learners

Audit Observations:

1. Faculties have maintained their course files.
2. Student's attendance was taken online and the same has been reflected in attendance registers.
3. Evaluation of internal assessments for respective courses are done as per the scheme and solution defined.
4. Class committee meetings (CCM) were conducted online.
5. Student mentor process did not happen due to pandemic.
6. Faculty feedback form of each faculty have been maintained.
7. Result analysis files has been maintained.
8. Microcontroller laboratory kits though sanctioned, but not purchased.
9. Internal audit of each semester has been carried out and proof of the same has been maintained.


20/7/2020
Dr. K. Chandra Selcher
Auditor
HOD, Department of Electronics
& Communication Engineering
Jyothy Institute of Technology
Tataguni, Off Kanakapura Road,
Bangalore. 560 062.



K S INSTITUTE OF TECHNOLOGY

Kanakapura Main Road, Raghuvanahalli, Bengaluru-560109

Internal Academic Audit Report

Department: Electronics and Communication Engineering

Academic Year: 2019-2020 (Even)

1. Faculties have maintained their course files properly.
2. Personal files are found to be maintained and updated.
3. One or two particulars of the file are missing as per check list like challenging questions and teaching pedagogy.

Auditors Signature

[Signature] 15/7/20
[Dr. Rekha.N.]

HEAD OF THE DEPARTMENT
Dept. of Telecommunication Engg
K. S. Institute of Technology
Bengaluru - 560 109

[Signature] 15/7/20
Devika.B

[Signature] 15/7/20
HOD-ECE

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Dept. of Electronics & Communication Engg
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K S I T
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Internal Academic Audit Report

Department: Electronics and Communication Engineering

Academic Year: 2019-2020 (Even)

Sl. No.	Name of the Staff	Sem & Section	Subject Name & Code	Strength noticed during Auditing	Weakness noticed during Auditing
1	Dr. P.N. SUDHA	VI A & B	Cellular Mobile Communication (17EC651)	Challenging Questions	Nil
		IV A	Control Systems (18EC43)	Teaching Pedagogy	Nil
		Personal File		Updated	Nil
2	Dr. SUREKHA B	VI A & B	Computer Communication Networks (17EC64)	Assignment question with scheme	Nil
		Personal File		Updated	Nil
3	Dr. SANGAPPA S.B	VIII A	Wireless Cellular and LTE 4G Broadband (15EC81)	IA question paper with scheme	Maintainance of documents
		Personal File		Updated	Nil
4	Dr. JOY PRABHAKARAN P.	VI A & B	Data Structures Using C++ (17EC661)	IA question paper with scheme	Nil
		Personal File		Updated	Nil
5	Dr. B. SUDHARSHAN	VI A & B	Digital Switching Systems (17EC654)	IA question paper with scheme	Teaching Pedagogy
		VI B	Embedded Controller Lab (17ECL67)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
6	Mr. SANTHOSH KUMAR B.R	IV A & B	Microcontroller (18EC46)	IA question paper with scheme	Assignment question with scheme
		IV A	Microcontroller Laboratory (18ECL47)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil

7	Mrs. VISHALINI DIVAKAR	II F	Basic Electrical Engineering (18ELE23)	IA question paper with scheme	Nil
		II EFG	Basic Electrical Engineering Laboratory (18ELEL27)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
8	Mrs. JAYASUDHA B.S.K	VI A&B	ARM Microcontroller & Embedded Systems (17EC62)	Assignment question with scheme	Challenging Questions
		VI A	Embedded Controller Lab (17ECL67)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
9	Mr. SAMPATH KUMAR S.	II B	Basic Electronics (18ELN24)	IA question paper with scheme	Challenging Questions
		II EFG	Basic Electrical Engineering (18ELEL27) Laboratory	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
10	Mr. SUNIL KUMAR G.R.	VI A	Digital System Design using Verilog (17EC663)	IA question paper with scheme	Challenging Questions
		IV A&B	Microcontroller Laboratory (18ECL47)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
11	Mrs. SANGEETHA V.	VII A&B	Digital Communication (17EC61)	Assignment question with scheme	Nil
		VI A	Computer Networks Lab (17ECL68)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
12	Mr. PRAVEEN A.	VI B	VLSI Design (17EC63)	IA question paper with scheme	Nil
		VI A&B	Computer Networks Lab (17ECL68)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
13	Mr. CHRISTO JAIN	IV A&B	Analog Circuits (18EC42)	Assignment question with scheme	Nil
		IV A	Analog Circuits Laboratory (18ECL48)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil

14	Mr. SALEEM S TEVARAMANI	VIII A&B	Radar Engineering (15EC833)	Challenging Questions	Nil
		VI B	Computer Networks Lab (17ECL68)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
15	Mrs. PRAGATHI P.	IV A&B	Signals & Systems (18EC45)	Assignment question with scheme	Nil
		IV B	Analog Circuits Laboratory (18ECL48)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
16	Mrs. SAHANA SALAGARE	VIII A&B	Fiber Optics & Networks (15EC82)	IA question paper with scheme	Challenging Questions
		IV B	Analog Circuits Laboratory(18ECL48)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
17	Mrs. ARUNA RAO B P	VI B	Digital System Design using Verilog (17EC663)	Question Bank	Nil
		VI A	VLSI Design (17EC63)	Assignment question with scheme	Nil
		VI A	Embedded Controller Lab (17ECL67)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
18	Mrs. SHILPA V	IV B	Control Systems (18EC43)	Challenging Questions	Nil
		VI B	Embedded Controller Lab (17ECL67)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
19	Mrs. BHARGAVI ANANTH	IV A & B	Engineering Statistics & Linear Algebra (18EC44)	Assignment question with scheme	Challenging Questions
		IV A	Analog Circuits Laboratory (18ECL48)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
20	Mrs. POOJA S	VIII B	Wireless Cellular and LTE 4G Broadband (15EC81)	Assignment question with scheme	Challenging Questions
		IV B	Microcontroller Laboratory (18ECL47)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil

21	Mrs. Smitha Mallya	II A	Basic Electronics (18ELN24)	Assignment question with scheme	Challenging Questions
		IV A&B	Microcontroller Laboratory (18ECL47)	Lab experiment videos are recorded and are made available to students.	Nil
		IV B	Analog Circuits Laboratory (18ECL48)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
22	Mrs. Preethi Mishra	II E	Basic Electrical Engineering (18ELE23)	Assignment question with scheme	Challenging Questions
		II EFG	Basic Electrical Engineering Laboratory (18EEL27)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil
23	Mrs. Priyadarshini J Patil	II G	Basic Electrical Engineering (18ELE23)	Assignment question with scheme	Pedagogy
		II A	Basic Electrical Engineering Laboratory (18EEL27)	Lab experiment videos are recorded and are made available to students.	Nil
		Personal File		Updated	Nil

Auditors Signature

15/7/20
[Rekha.N.]

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Devika B

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Internal Academic Audit Report


Department: Electronics and Communication Engineering

Academic Year: 2019-2020 (odd)

1. Course file of the faculties were maintained properly. Faculties have adhered to the rules and regulations defined by the Head of the department.
2. Personal files are maintained and updated as per the check list.
3. Faculties have to incorporate more pedagogies in their teaching-learning process.

Auditors Signature

1. Rekha.N.  9/1/2020

2. Devika.B  9/1/2020

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HOD-ECE
9/1/2020

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Internal Academic Audit Report

Department: Electronics and Communication Engineering

Academic Year: 2019-2020 (odd)

Sl. No.	Name of the Staff	Sem & Section	Subject Name & Code	Strength noticed during Auditing	Weakness noticed during Auditing
1	Dr. P.N. SUDHA	VII A & B	Cryptography (15EC744)	Challenging Questions. Teaching Pedagogy.	Nil
		Personal File		Updated	Nil
2	Dr. SUREKHA B	VII A & B	Microwave and Antennas (15EC71)	Challenging Questions, Teaching Pedagogy	Nil
		V B	HDL Lab (17ECL58)	All the experiments completed. Marks are entered in attendance register	Nil
		Personal File		Updated	Nil
3	Dr. SANGAPPA S.B	V A & B	Management and Entrepreneurship Development (17ES51)	Assignment question with scheme	Maintainance of documents
		Personal File		Updated	Nil
4	Dr. JOY PRABHAKARAN P.	V A & B	Object Oriented Programming Using C++ (17EC562)	Challenging Questions, Teaching Pedagogy	Nil
		Personal File		Updated	Nil
5	Dr. B. SUDHARSHAN	V A & B	Operating System (17EC553)	Assignment question with scheme	Teaching Pedagogy
		V B	DSP Lab (17ECL57)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
6	Mr. SANTHOSH KUMAR B.R	III B	Digital System Design (18EC33)	Challenging Questions, Teaching Pedagogy	Nil
		V A & B	8051 Microcontroller (17EC563)	Assignment question with scheme	Nil
		III B	Digital System Design Laboratory (18ECL38)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil

7	Mrs. VISHALINI DIVAKAR	I A&C	Basic Electrical Engineering (18ELE13)	IA question paper with scheme	Maintainance of documents
		I A&C	Basic Electrical Engineering Laboratory (18ELEL17)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
8	Mrs. JAYASUDHA B.S.K	V A&B	Digital Signal Processing (17EC52)	Assignment question with scheme	Nil
		V A	DSP Lab (17ECL57)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
9	Mr. SAMPATH KUMAR S.	I F	Basic Electronics (18ELN14)	IA question paper with scheme	Maintainance of documents
		I A,B,C	Basic Electrical Engineering Laboratory (18ELEL17)	All the experiments completed. Marks are entered in attendance register.	Nil
		VII A & B	Advanced Communication Lab (15ECL76)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
10	Mr. SUNIL KUMAR G.R.	V A	Information Theory & Coding (17EC54)	IA question paper with scheme	Teaching Pedagogy
		V A&B	HDL Lab (17ECL58)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
11	Mrs. SANGEETHA V.	VII A&B	Satellite Communication (15EC755)	Challenging Questions, Teaching Pedagogy	Nil
		V B	DSP Lab (17ECL57)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
12	Mr. PRAVEEN A.	V B	Information Theory & Coding (17EC54)	IA question paper with scheme	Teaching Pedagogy
		VII A&B	VLSI Lab (15ECL77)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil

13	Mr. CHRISTO JAIN	VII A&B	Power Electronics (15EC73)	Challenging Questions, Teaching Pedagogy	Nil
		V A	DSP Lab (17ECL57)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
14	Mr. SALEEM S TEVARAMANI	VII A&B	Digital Image Processing (15EC72)	Challenging Questions, Teaching Pedagogy	Nil
		VII A&B	Advanced Communication Lab (15ECL76)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
15	Mrs. NITHYA KUMARI R	III A&B	Electronic Devices (18EC32)	Challenging Questions, Question Bank	Nil
		III B	Electronic Devices & Instrumentation Laboratory (18ECL37)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
16	Mrs. PRAGATHI P.	III A&B	Network Theory (18EC34)	Assignment question with scheme	Teaching Pedagogy
		III A	Electronic Devices & Instrumentation Laboratory (18ECL37)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
17	Mrs. SAHANA SALAGARE	I E	Basic Electronics (18ELN14)	IA question paper with scheme	Nil
		III A	Electronic Devices & Instrumentation Laboratory (18ECL37)	All the experiments completed. Marks are entered in attendance register.	Nil
		VII A	Advanced Communication Lab (15ECL76)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
		V A&B	Verilog HDL (17EC53)	Challenging Questions, Question Bank	Nil

18	Mrs. ARUNA RAO B P	V A	HDL Lab (17ECL58)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
19	Mrs. VEDA B	III A	Digital System Design (18EC34)	Challenging Questions, Question Bank	Nil
		III A&B	Digital System Design Laboratory (18ECL38)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
20	Mrs. SHILPA V	III B	Power Electronics & Instrumentation (18EC36)	Challenging Questions, Teaching Pedagogy	Nil
		III B	Electronic Devices & Instrumentation Laboratory (18ECL37)	All the experiments completed. Marks are entered in attendance register.	Nil
		VII A&B	VLSI Lab (15ECL77)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
21	Mrs. BHARGAVI ANANTH	III A & B	Computer Organization & Architecture (18EC35)	IA question paper with scheme	Nil
		I B	Basic Electrical Engineering Laboratory (18ELE17)	All the experiments completed. Marks are entered in attendance register.	Nil
		VII B	Advanced Communication Lab (15ECL76)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
22	Mrs. POOJA S	III A	Power Electronics & Instrumentation (18EC36)	Teaching Pedagogy	Challenging Questions
		III A	Digital System Design Laboratory (18ECL38)	All the experiments completed. Marks are entered in attendance register.	Nil
		VII A&B	VLSI Lab (15ECL77)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil
		I B	Basic Electrical Engineering (18ELE13)	Question Bank	Teaching Pedagogy

23	Mrs. MOHANAMBICA K M	I A,B,C	Basic Electrical Engineering Laboratory (18ELEEL17)	All the experiments completed. Marks are entered in attendance register.	Nil
		Personal File		Updated	Nil

Auditors Signature

1. Rekha N. 9/1/2020
2. Devika B 9/1/2020

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