



K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IV SEMESTER TIME TABLE FOR THE YEAR 2020 (EVEN SEMESTER)

W.E.F. : 10/02/2020

SEC : 'A'

CLASS TEACHER : Mrs. BHARGAVI ANANTH

CLASS ROOM : OB LH 201

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME	8.30 AM	9.25 AM		10.35 AM	11.30 AM		1.15 PM	2.10 PM	3.05 PM
DAY	9.25 AM	10.20 AM		11.30 AM	12.25 PM		2.10 PM	3.05 PM	4.00 PM
MON	MATHS (18MAT41)	CS (18EC43)	T E A	MC (18EC46)	SS (18EC45)	L U N C H B R E A K	ES&LA (18EC44)	AC (18EC42)	CIP (18CPC39/49)
TUE	← MC LAB (18ECL47) - A1 AC LAB (18ECL48) - A2 →			MC (18EC46)			MATHS (18MAT41)	SS (18EC45)	CS (18EC43) (T)
WED	CS (18EC43)	ES&LA (18EC44)	B R E	SS (18EC45)	AC (18EC42)		← MC LAB (18ECL47) - A2 AC LAB (18ECL48) - A3 →		
THU	← MC LAB (18ECL47) - A3 AC LAB (18ECL48) - A1 →			CS (18EC43)			MATHS (18MAT41)	ES&LA (18EC44)	MC (18EC46)
FRI	CS (18EC43)	AC (18EC42)	A K	MATHS (18MAT41)	ES&LA (18EC44)		SS (18EC45)	MC (18EC46)	AC (18EC42)

Sub-Code	Subject Name	Faculty Name
18MATDIP41	Additional Mathematics – II	
18MAT41	Complex Analysis, Probability and Statistical Methods	Mr. Chowdappa M R
18EC42	Analog Circuits	Mr. Christo Jain
18EC43	Control Systems	Dr. P.N Sudha
18EC44	Engineering Statistics & Linear Algebra	Mrs. Bhargavi Ananth
18EC45	Signals & Systems	Mrs. Pragati. P
18EC46	Microcontroller	Mr. B.R.Santhosh Kumar
18ECL47	Microcontroller Laboratory	Mr. B.R.Santhosh Kumar, Mr. Sunil Kumar.G.R, Mrs. Smitha Mallya
18ECL48	Analog Circuits Laboratory	Mr. Christo Jain, Mrs. Bhargavi Ananth
18CPC39/49	Constitution of India, Professional Ethics and Cyber Law	Mrs. Anuradha

Time Table Co-ordinator

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W.E.F. : 10/02/2020

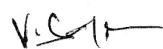
SEC : 'B'

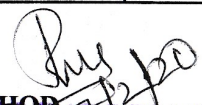
CLASS TEACHER : Mrs. SHILPA V

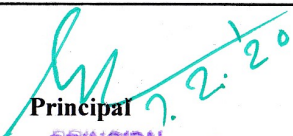
CLASS ROOM : OB LH 203

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	MC (18EC46)	MATHS (18MAT41)	T E A B R E A K	ES&LA (18EC44)	AC (18EC42)	L U N C H B R E A K	MC LAB (18ECL47) - B1 AC LAB (18ECL48) - B2		
TUE	CS (18EC43)	SS (18EC45)		CS (18EC43)	ES&LA (18EC44)		AC (18EC42)	MATHS (18MAT41)	ES&LA (18EC44)
WED	AC (18EC42)	MC (18EC46)		MATHS (18MAT41)	MC (18EC46)		CIP (18CPC39/49)	SS (18EC45)	CS (18EC43)
THU	SS (18EC45)	MC (18EC46)		SS (18EC45)	MATHS (18MAT41)		MC LAB (18ECL47) - B2 AC LAB (18ECL48) - B3		
FRI	MC LAB (18ECL47) - B3 AC LAB (18ECL48) - B1			CS (18EC43)	AC (18EC42)		ES&LA (18EC44)	DIP MATHS (18MATDIP41)	

Sub-Code	Subject Name	Faculty Name
18MATDIP41	Additional Mathematics – II	
18MAT41	Complex Analysis, Probability and Statistical Methods	Mrs. Jalaja P
18EC42	Analog Circuits	Mr. Christo Jain / Mr. Sampath Kumar
18EC43	Control Systems	Mrs. Shilpa V
18EC44	Engineering Statistics & Linear Algebra	Mrs. Bhargavi Ananth
18EC45	Signals & Systems	Mrs. Pragati. P
18EC46	Microcontroller	Mr. B.R. Santhosh Kumar
18ECL47	Microcontroller Laboratory	Mr. Sunil Kumar. G.R, Mrs. Pooja S
18ECL48	Analog Circuits Laboratory	Mrs. Pragati. P, Mrs. Sahana Salagare, Mrs. Smitha Mallya
18CPC39/49	Constitution of India, Professional Ethics and Cyber Law	Mrs. Anuradha


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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
VI SEMESTER TIME TABLE FOR THE YEAR 2020 (EVEN SEMESTER)

W.E.F. : 10/02/2020

SEC : 'A'

CLASS TEACHER : Mrs. JAYASUDHA B S K

CLASS ROOM : OB LH 204

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	CMC(17EC651)/ DSS(17EC654)	VLSI (17EC63)	T E A	ARM (17EC62)	CCN (17EC64)	L U N C H B R E A K	EC LAB (17ECL67) - A1 CN LAB (17ECL68) - A2		
TUE	CMC(17EC651)/ DSS(17EC654)	ARM (17EC62)		DSDV(17EC663)/ C++ (17EC661)	ARM (17EC62)		VLSI (17EC63)	DC (17EC61)	T
WED	EC LAB (17ECL67) - A2 CN LAB (17ECL68) - A3				DSDV(17EC663)/ C++ (17EC661)		DC (17EC61)	CMC(17EC651)/ DSS(17EC654)	DC (17EC61)
THU	CCN (17EC64)	VLSI (17EC63)	B R E A K	CCN (17EC64)	DSDV(17EC663)/ C++ (17EC661)		EC LAB (17ECL67) - A3 CN LAB (17ECL68) - A1		
FRI	CCN (17EC64)	ARM (17EC62)		CMC(17EC651)/ DSS(17EC654)	DSDV(17EC663)/ C++ (17EC661)		DC (17EC61)	VLSI (17EC63)	T

Sub-Code	Subject Name	Faculty Name
17EC61	Digital Communication	Mrs.Sangeetha.V
17EC62	ARM Microcontroller & Embedded Systems	Mrs. Jayasudha B.S.K
17EC63	VLSI Design	Mrs. Aruna Rao B P
17EC64	Computer Communication Networks	Dr. B Surekha
17EC651	Cellular Mobile Communication (Professional Elective-2)	Dr. P.N Sudha
17EC654	Digital Switching Systems (Professional Elective-2)	Dr. B.Sudharshan
17EC661	Data Structures Using C++ (Open Elective-2)	Dr. P Joy Prabhakaran
17EC663	Digital System Design using Verilog (Open Elective-2)	Mr. Sunil Kumar.G.R
17ECL67	Embedded Controller Lab	Dr. B.Sudharshan, Mrs. Aruna Rao B.P
17ECL68	Computer Networks Lab	Mr. Praveen.A, Mrs.Sangeetha.V, Mr. Saleem S Tevaramani

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VI SEMESTER TIME TABLE FOR THE YEAR 2020 (EVEN SEMESTER)

W.E.F. : 10/02/2020

SEC : 'B'

CLASS TEACHER : Mrs. ARUNA RAO B P

CLASS ROOM : OB LH 205

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7	
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM	
MON	CMC(17EC651)/ DSS(17EC654)	DC (17EC61)	T E A C H E R	VLSI (17EC63)	DC (17EC61)	L U N C H B R E A K	CCN (17EC64)	ARM (17EC62)	T	
TUE	CMC(17EC651)/ DSS(17EC654)	VLSI (17EC63)		DSDV(17EC663)/ C++(17EC661)	CCN (17EC64)		← EC LAB (17ECL67) - B1 CN LAB (17ECL68) - B2 →			
WED	CCN (17EC64)	ARM (17EC62)		CCN (17EC64)	DSDV(17EC663)/ C++ (17EC661)		VLSI (17EC63)	CMC(17EC651)/ DSS(17EC654)	ARM (17EC62)	
THU	← EC LAB (17ECL67) - B2 CN LAB (17ECL68) - B3 →				DSDV(17EC663)/ C++ (17EC661)		DC (17EC61)	ARM (17EC62)	T	
FRI	DC (17EC61)	VLSI (17EC63)		A K	CMC(17EC651)/ DSS(17EC654)		DSDV(17EC663)/ C++ (17EC661)	← EC LAB (17ECL67) - B3 CN LAB (17ECL68) - B1 →		

Sub-Code	Subject Name	Faculty Name
17EC61	Digital Communication	Mrs.Sangeetha.V
17EC62	ARM Microcontroller & Embedded Systems	Mrs. Jayasudha B.S.K
17EC63	VLSI Design	Mr. Praveen.A
17EC64	Computer Communication Networks	Dr. B Surekha
17EC651	Cellular Mobile Communication (Professional Elective-2)	Dr. P.N Sudha
17EC654	Digital Switching Systems (Professional Elective-2)	Dr. B.Sudharshan
17EC661	Data Structures Using C++ (Open Elective-2)	Dr. P Joy Prabhakaran
17EC663	Digital System Design using Verilog (Open Elective-2)	Mrs. Aruna Rao B P
17ECL67	Embedded Controller Lab	Mrs. Jayasudha BSK, Mrs.Shilpa
17ECL68	Computer Networks Lab	Mr. Praveen.A, Mrs.Sangeetha.V, Mr. Saleem S Tevaramani,

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VI SEMESTER TIME TABLE FOR THE YEAR 2020 (EVEN SEMESTER)

W.E.F. : 10/02/2020

SEC : 'A'

Parallel Scheme -2015

CLASS TEACHER : Mrs. JAYASUDHA B S K

CLASS ROOM : OB LH 204

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	DSS(15EC654)	VLSI (15EC63)	T E A	ARM (15EC62)	CCN (15EC64)	L U N C H B R E A K	EC LAB (15ECL67) - A1 CN LAB (15ECL68) - A2		
TUE	DSS(15EC654)	ARM (15EC62)		DSDV(15EC663)	ARM (15EC62)		VLSI (15EC63)	DC (15EC61)	T
WED	EC LAB (15ECL67) - A2 CN LAB (15ECL68) - A3				DSDV(15EC663)		DC (15EC61)	DSS(15EC654)	DC (15EC61)
THU	VLSI (15EC63)	CCN (15EC64)	B R E A K	VLSI (15EC63)	DSDV(15EC663)		EC LAB (15ECL67) - A3 CN LAB (15ECL68) - A1		
FRI	CCN (15EC64)	ARM (15EC62)		DSS(15EC654)	DSDV(15EC663)		DC (15EC61)	CCN (15EC64)	T

Sub-Code	Subject Name	Faculty Name
15EC61	Digital Communication	Mrs.Sangeetha.V
15EC62	ARM Microcontroller & Embedded Systems	Mrs. Jayasudha B.S.K
15EC63	VLSI Design	Mrs. Aruna Rao B P
15EC64	Computer Communication Networks	Dr. B Surekha
15EC654	Digital Switching Systems (Professional Elective-2)	Dr. B.Sudharshan
15EC663	Digital System Design using Verilog (Open Elective-2)	Mr. Sunil Kumar.G.R
15ECL67	Embedded Controller Lab	Dr. B.Sudharshan, Mrs. Aruna Rao B.P
15ECL68	Computer Networks Lab	Mr. Praveen.A, Mrs.Sangeetha.V, Mr. Saleem S Tevaramani

V. S. K.
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P. S. K.
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7/2/20
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S. S. K.
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VI SEMESTER TIME TABLE FOR THE YEAR 2020 (EVEN SEMESTER)

W.E.F. : 10/02/2020

SEC : 'B'

Parallel Scheme - 2015

CLASS TEACHER : Mrs. ARUNA RAO B P

CLASS ROOM : OB LH 205

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7	
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM	
MON	DSS(15EC654)	DC (15EC61)	T E A C H E R	VLSI (15EC63)	DC (15EC61)	L U N C H B R E A K	CCN (15EC64)	ARM (15EC62)	T	
TUE	DSS(15EC654)	VLSI (15EC63)		DSDV(15EC663)	CCN (15EC64)		← EC LAB (15ECL67) - B1 CN LAB (15ECL68) - B2 →			
WED	CCN (15EC64)	ARM (15EC62)		CCN (15EC64)	DSDV(15EC663)		VLSI (15EC63)	DSS(15EC654)	ARM (15EC62)	
THU	← EC LAB (15ECL67) - B2 CN LAB (15ECL68) - B3 →				DSDV(15EC663)		DC (15EC61)	ARM (15EC62)	T	
FRI	DC (15EC61)	VLSI (15EC63)		A K	DSS(15EC654)		DSDV(15EC663)	← EC LAB (15ECL67) - B3 CN LAB (15ECL68) - B1 →		

Sub-Code	Subject Name	Faculty Name
15EC61	Digital Communication	Mrs.Sangeetha.V
15EC62	ARM Microcontroller & Embedded Systems	Mrs. Jayasudha B.S.K
15EC63	VLSI Design	Mr. Praveen.A
15EC64	Computer Communication Networks	Dr. B Surekha
15EC654	Digital Switching Systems (Professional Elective-2)	Dr. B.Sudharshan
15EC663	Digital System Design using Verilog (Open Elective-2)	Mrs. Aruna Rao B P
15ECL67	Embedded Controller Lab	Mrs. Jayasudha BSK, Mrs.Shilpa
15ECL68	Computer Networks Lab	Mrs.Sangeetha.V, Mr. Saleem S Tevaramani,

V. S. Srinivas
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VI SEMESTER TIME TABLE FOR THE YEAR 2020 (EVEN SEMESTER)

W.E.F. : 10/02/2020

SEC : 'B'

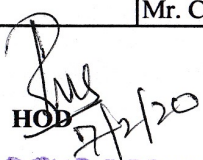
Parallel Scheme (2010 SCHEME)

CLASS TEACHER : Mrs. ARUNA RAO B P

CLASS ROOM : OB LH 205

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7	
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM	
MON	SC (10EC662)	DC (10EC61)	T E A C H E R	MP (10EC62)	DC (10EC61)	L U N C H B R E A K	MP (10EC62)	MEC (10EC63)	T	
TUE	OS (10EC65)	AP (10EC64)		OS (10EC65)	SC (10EC662)		← ADC LAB (10ECL67) →			
WED	MEC (10EC63)	AP (10EC64)		AP (10EC64)	MP (10EC62)		MEC (10EC63)	SC (10EC662)		
THU	← MP LAB (10ECL68) →				SC (10EC662)		DC (10EC61)	← ADC LAB (10ECL67) →		
FRI	DC (10EC61)	OS (10EC65)	E A K	MP (10EC62)	OS (10EC65)		MEC (10EC63)	AP (10EC64)		
Sub-Code		Subject Name				Faculty Name				
10EC61		Digital Communication				Mrs.Sangeetha . V				
10EC62		Microprocessors				Mr. Sunil Kumar				
10EC63		Microelectronics Circuits				Mrs. Aruna Rao B.P				
10EC64		Antennas & Propagation				Mr. Christo Jain				
10EC65		Operating Systems				Dr. Sudarshan . B				
10EC662		Satellite communication				Mr. Saleem S Tevaramani				
10ECL67		Advanced Communication Lab				Mr. Sampath Kumar S				
10ECL68		Microprocessors Lab				Mr. Christo Jain				


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VIII SEMESTER TIME TABLE FOR THE YEAR 2020 (EVEN SEMESTER)

W.E.F. : 10/02/2020

SEC : 'A'

CLASS TEACHER : Mrs. SAHANA SALAGARE

CLASS ROOM : OB LH 310

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	FON (15EC82)	WC (15EC81)	T E A B R E A K	RE (15EC833)	FON (15EC82)	L U N C H B R E A K			
TUE	WC (15EC81)	RE (15EC833)		FON (15EC82)	WC (15EC81)		← Internship/Professional Practice (15EC84) →		
WED	FON (15EC82)	RE (15EC833)		WC (15EC81)	RE (15EC833)		← Seminar (15ECS86) →		
THU	← Project Work (15ECP85) →								
FRI	← Project Work (15ECP85) →								

Sub-Code	Subject Name	Faculty Name
15EC81	Wireless Cellular and LTE 4G Broadband	Dr. Sangappa.S.B
15EC82	Fiber Optics & Networks	Mrs. Sahana Salagare
15EC833	Radar Engineering (Professional Elective-5)	Mr. Saleem S Tevaramani
15EC84	Internship/Professional Practice	Mr. B.R.Santhosh Kumar, Mrs. Aruna Rao B P, Mrs. Smitha Mallya
15ECP85	Project Work	Dr. B.Sudharshan, Mrs. Sahana Salagare
15ECS86	Seminar	Dr. Surekha B , Mrs. Pooja S

V. Srinivas
Time Table Co-ordinator

P. S. Srinivas
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SEC : 'B'

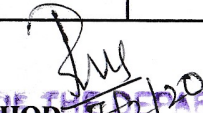
CLASS TEACHER : Mrs. POOJA S

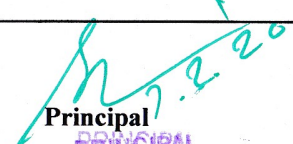
CLASS ROOM : OB LH 311

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7	
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM	
MON	RE (15EC833)	WC (15EC81)	T E A C H E R B R E A K	FON (15EC82)	RE (15EC833)	L U N C H B R E A K				
TUE	WC (15EC81)	FON (15EC82)		WC (15EC81)	RE (15EC833)		← Seminar (15ECS86) →			
WED	RE (15EC833)	FON (15EC82)		WC (15EC81)	FON (15EC82)		← Internship/Professional Practice (15EC84) →			
THU	← Project Work (15ECP85) →									
FRI	← Project Work (15ECP85) →									

Sub-Code	Subject Name	Faculty Name
15EC81	Wireless Cellular and LTE 4G Broadband	Mrs. Pooja S
15EC82	Fiber Optics & Networks	Mrs. Sahana Salagare
15EC833	Radar Engineering (Professional Elective-5)	Mr. Saleem S Tevaramani
15EC84	Internship/Professional Practice	Mrs. Aruna Rao B P, Mr. Saleem S Tevaramani, Mr. Sunil Kumar G R
15ECP85	Project Work	Dr. B.Sudharshan, Mrs. Sahana Salagare
15ECS86	Seminar	Dr. Surekha B , Mrs. Pooja S


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